

## 30 Amp, No Heatsink, Isolated DC/DC Converter

*The PQ48053HTA30 PowerQor® Tera converter is a next-generation, board-mountable, isolated, fixed switching frequency DC/DC converter that uses synchronous rectification to achieve extremely high conversion efficiency. The power dissipated by the converter is so low that a heatsink is not required, which saves cost, weight, height, and application effort. The Tera series converters offer the maximum useable output current for any standard "half-brick" module.*

PowerQor  
Tera



PQ48053HTA30 Module

### Operational Features

- Ultra-high efficiency, 92% at full rated load current
- Delivers up to 30 amps of output current with minimal derating - no heatsink required
- Wide input voltage range: 39V – 75V
- Fixed frequency switching provides predictable EMI performance

### Mechanical Features

- Industry standard pin-out configuration
- Industry standard size: 2.3" x 2.4"
- Total height only 0.44", permits better airflow and smaller card pitch
- Total weight: 2.6 oz. (75 grams)

### Safety Features

- 2000V, 10 M $\Omega$  input-to-output isolation
- UL/cUL 60950 recognized (US & Canada), basic insulation rating
- TUV certified to EN60950
- Meets 72/23/EEC and 93/68/EEC directives
- Meets UL94V-0 flammability requirements

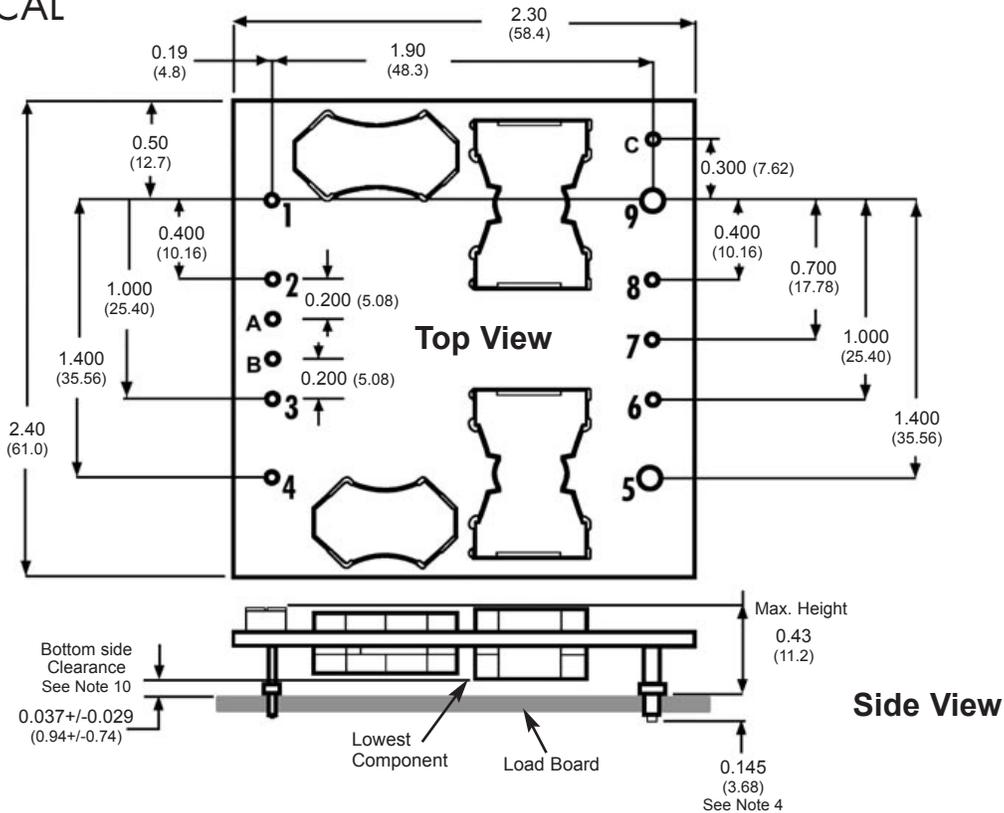
### Protection Features

- Input under-voltage lockout and over-voltage shutdown protects against abnormal input voltages
- Output current limit and short circuit protection
- Output over-voltage protection
- Thermal shutdown

### Control Features

- On/Off control referenced to input side (negative logic)
- Remote sense for the output voltage compensates for output distribution drops
- Output voltage trim permits custom voltages and voltage margining

### MECHANICAL DIAGRAM



### NOTES

- 1) Pins 1-4, 6-8, A-C are 0.040" (1.02mm) diameter. with 0.080" (2.03mm) diameter standoff shoulders.
- 2) Pins 5 and 9 are 0.080" (2.03 mm) diameter with 0.125" (3.18mm) diameter standoff shoulders.
- 3) Pins 3, A, B & C only included in Full-Feature models.
- 4) Other pin extension lengths available. Recommended pin length is 0.03" (0.76mm) greater than the PCB thickness.
- 5) All Pins: Material - Copper Alloy  
Finish - Tin/Lead over Nickel plate
- 6) Undimensioned components are shown for visual reference only.
- 7) All dimensions in inches (mm)  
Tolerances: x.xx +/-0.02 in. (x.x +/-0.5mm)  
              x.xxx +/-0.010 in. (x.xx +/-0.25mm)
- 8) Weight: 2.6 oz. (75g) typical
- 9) Workmanship: Meets or exceeds IPC-A-610C Class II
- 10) UL/TUV standards require a clearance greater than 0.04" (1.02mm) between input and output for Basic insulation. This issue should be considered if any copper traces are on the top side of the user's board. Note that the ferrite cores are considered part of the input/primary circuit.

### PIN CONNECTIONS

Pin No.	Name	Function
1	Vin(+)	Positive input voltage
2	ON/OFF	TTL input to turn converter on and off, referenced to Vin(-), with internal pull up.
3	<i>I share</i>	<i>Single wire parallel signal</i>
4	Vin(-)	Negative input voltage
5	Vout(-)	Negative output voltage
6	SENSE(-)	Return remote sense
7	TRIM	Output voltage trim
8	SENSE(+)	Positive remote sense
9	Vout(+)	Positive output voltage
A	<i>Clock Sync</i>	<i>Clock synchronization</i>
B	<i>Start Sync</i>	<i>Startup synchronization</i>
C	<i>OR Drive</i>	<i>External OR-FET drive</i>

*Pins in Italics Shaded text are not available*



# Technical Specification

**Half  
Brick**

**48V<sub>in</sub> 5.3V<sub>out</sub> 30A**

## PQ48053HTA30 ELECTRICAL CHARACTERISTICS

T<sub>A</sub>=25°C, airflow rate=300 LFM, V<sub>in</sub>=48Vdc unless otherwise noted; full operating temperature range is -40°C to +100°C ambient temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Min.	Typ.	Max.	Units	Notes & Conditions
<b>ABSOLUTE MAXIMUM RATINGS</b>					
Input Voltage					
Non-Operating			100	V	continuous
Operating			80	V	continuous
Operating Transient Protection			100	V	≤ 10μs
Isolation Voltage (input to output)			2000	V	Basic insulation level, Pollution degree 2
Operating Temperature	-40		100	°C	
Storage Temperature	-55		125	°C	
Voltage at ON/OFF input pin	-2		18	V	
Voltage at Clock Sync pin	N/A		N/A	V	applies to Full Feature option only
<b>INPUT CHARACTERISTICS</b>					
Operating Input Voltage Range	39	48	75	V	
Input Under-Voltage Lockout					
Turn-On Voltage Threshold	31.5	33.3	34.4	V	
Turn-Off Voltage Threshold	29.5	31.0	32.4	V	
Lockout Hysteresis Voltage		2.3		V	
Input Over-Voltage Shutdown	76.7	78.3	79.5	V	
Maximum Input Current			5.1	A	100% Load, 35 V <sub>in</sub>
No-Load Input Current		100	125	mA	
Disabled Input Current		2	5	mA	
Inrush Current Transient Rating		0.03		A <sup>2</sup> s	
Response to Input Transient		150		mV	1000V/ms input transient
Input Reflected-Ripple Current		5		mA	RMS thru 10μH inductor; Figs. 13 & 15
Recommended Input Fuse			20	A	fast blow external fuse recommended
Input Filter Component Values (C1\LC2)		1.6\4\3.3		μF\μH\μF	internal values, see Figure E
Recommended External Input Capacitance	33	47		μF	see Figure 13
<b>OUTPUT CHARACTERISTICS</b>					
Output Voltage Set Point	5.250	5.300	5.350	V	
Output Voltage Regulation					
Over Line		±0.05 \ 3	±0.1 \ 5	%\mV	
Over Load		±0.1 \ 5	±0.2 \ 10	%\mV	
Over Temperature		±15	±30	mV	
Total Output Voltage Range	5.205		5.395	V	over sample, line, load, temperature & life
Output Voltage Ripple and Noise					20MHz bandwidth; Fig. 13 & 16
Peak-to-Peak		50	100	mV	Full Load, see Figures 13 & 16
RMS		10	20	mV	Full Load, see Figures 13 & 16
Operating Output Current Range	0		30	A	
Output DC Current-Limit Inception	31.5	34	36.4	A	Output Voltage 10% Low; Fig. 17
Output DC Current-Limit Shutdown Voltage		3.4		V	
Current Share Accuracy (2 units paralleled)		NA		%	% of rated output current
Back-Drive Current Limit while Enabled		1.0		A	Max negative current drawn from output
Back-Drive Current Limit while Disabled		TBD		mA	Max negative current drawn from output
Maximum Output Capacitance			>50,000	μF	5.3V <sub>out</sub> at 30A Resistive Load
<b>DYNAMIC CHARACTERISTICS</b>					
Input Voltage Ripple Rejection		80		dB	120 Hz
Output Voltage during Load Current Transient					
Step Change in Output Current (0.1A/μs)		200		mV	50%-75%-50% lout max, 15μF load cap
Step Change in Output Current (5A/μs)		200		mV	50% to 75% to 50% lout max
Settling Time		300		μs	to within 1% V <sub>out</sub> nom
Turn-On Transient					
Turn-On Time		20	30	ms	Full load, V <sub>out</sub> =90% nom.
Start-Up Inhibit Time	180	200	215	ms	-40°C to +125°C; Figure A
Output Voltage Overshoot		0		%	10,000 μF load capacitance, lout = 0A
<b>EFFICIENCY</b>					
100% Load		92		%	Figures 1 - 4
50% Load		91		%	Figures 1 - 4
<b>TEMPERATURE LIMITS FOR POWER DERATING CURVES</b>					
Semiconductor Junction Temperature			125	°C	Package rated to 150°C
Board Temperature			125	°C	UL rated max operating temp 130°C
Transformer Temperature			125	°C	See Figures 5 - 6 for derating curves
<b>ISOLATION CHARACTERISTICS</b>					
Isolation Voltage		2000		V	
Isolation Resistance		10		MΩ	
Isolation Capacitance			3300	pF	

### ELECTRICAL CHARACTERISTICS (Continued)

Parameter	Min.	Typ.	Max.	Units	Notes & Conditions
<b>FEATURE CHARACTERISTICS</b>					
Switching Frequency	205	230	255	kHz	
ON/OFF Control (Option P)					
Off-State Voltage	N/A		N/A	V	
On-State Voltage	N/A		N/A	V	
ON/OFF Control (Option N)					
Off-State Voltage	2.7		18	V	
On-State Voltage	-2		0.8	V	
ON/OFF Control (Either Option)					Figures A, B
Pull-Up Voltage		V <sub>in</sub> /6	15	V	
Pull-Up Resistance		42		kΩ	
Output Voltage Trim Range	-20		+10	%	Measured across Pins 9 & 5; Figure C
Output Voltage Remote Sense Range			+10	%	Measured across Pins 9 & 5
Output Over-Voltage Protection	112	119	126	%	Over full temp range; % of nominal V <sub>out</sub>
Over-Temperature Shutdown		120		°C	Average PCB Temperature
Over-Temperature Shutdown Restart Hysteresis		10		°C	
<b>RELIABILITY CHARACTERISTICS</b>					
Calculated MTBF (Telcordia)		2.2		10 <sup>6</sup> Hrs.	TR-NWT-000332; 80% load, 300LFM, 40°C T <sub>a</sub>
Calculated MTBF (MIL-217)		1.3		10 <sup>6</sup> Hrs.	MIL-HDBK-217F; 80% load, 300LFM, 40°C T <sub>a</sub>
Demonstrated MTBF		>20		10 <sup>6</sup> Hrs.	Field demonstrated MTBF

### STANDARDS COMPLIANCE

Parameter	Notes
<b>STANDARDS COMPLIANCE</b>	
UL/cUL 60950	File # E194341, Basic insulation & pollution degree 2
EN60950	Certified by TUV
72/23/EEC	
93/68/EEC	
Needle Flame Test (IEC 695-2-2)	test on entire assembly; board & plastic components UL94V-0 compliant
IEC 61000-4-2	ESD test, 8kV - NP, 15kV air - NP
GR-1089-CORE	Section 7 - electrical safety, Section 9 - bonding/grounding

- An external input fuse must always be used to meet these safety requirement

### QUALIFICATION TESTING

Parameter	# Units	Test Conditions
<b>QUALIFICATION TESTING</b>		
Life Test	32	95% rated V <sub>in</sub> and load, units at derating point, 1000 hours
Vibration	5	10-55Hz sweep, 0.060" total excursion, 1 min./sweep, 120 sweeps for 3 axis
Mechanical Shock	5	100g minimum, 2 drops in x and y axis, 1 drop in z axis
Temperature Cycling	10	-40°C to 100°C, unit temp. ramp 15°C/min., 500 cycles
Power/Thermal Cycling	5	T <sub>operating</sub> = min to max, V <sub>in</sub> = min to max, full load, 100 cycles
Design Marginality	5	T <sub>min</sub> -10°C to T <sub>max</sub> +10°C, 5°C steps, V <sub>in</sub> = min to max, 0-105% load
Humidity	5	85°C, 85% RH, 1000 hours, 2 minutes on and 6 hours off
Solderability	15 pins	MIL-STD-883, method 2003

- Extensive characterization testing of all SynQor products and manufacturing processes is performed to ensure that we supply robust, reliable product. Contact factory for more information about Proof of Design and Proof of Manufacturing processes.

### OPTIONS

SynQor provides various options for Logic Sense, Pin Length and Feature Set for this family of DC/DC converters. Please consult the last page of this specification sheet for information on available options.

## Performance Curves

**Half Brick**     **48V<sub>in</sub> 5.3V<sub>out</sub> 30A**

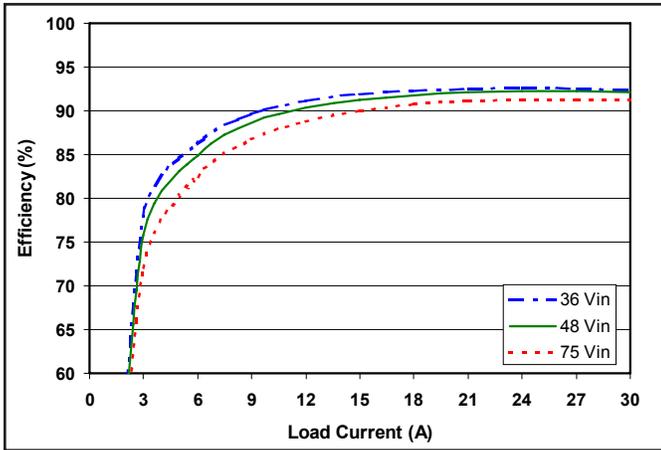


Figure 1: Efficiency at nominal output voltage vs. load current for minimum, nominal, and maximum input voltage at 25°C.

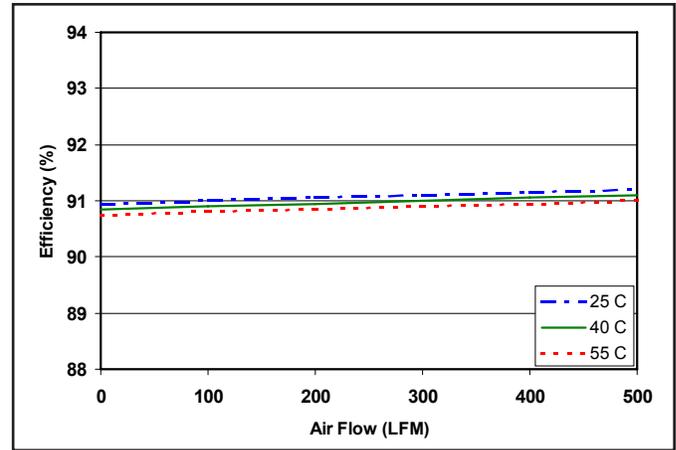


Figure 2: Efficiency at nominal output voltage and 60% rated power vs. airflow rate for ambient air temperatures of 25°C, 40°C, and 55°C and nominal input voltage.

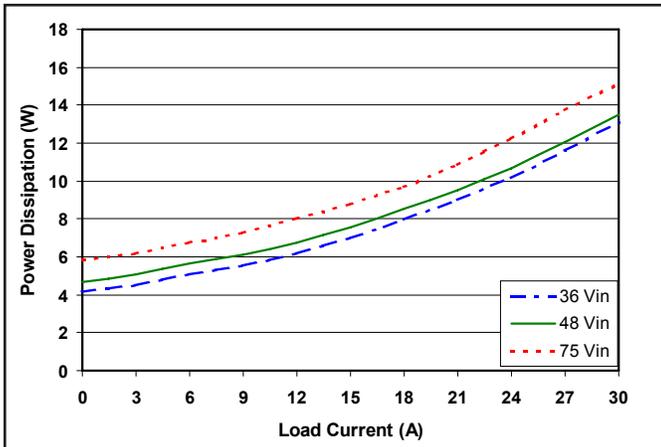


Figure 3: Power dissipation at nominal output voltage vs. load current for minimum, nominal, and maximum input voltage at 25°C.

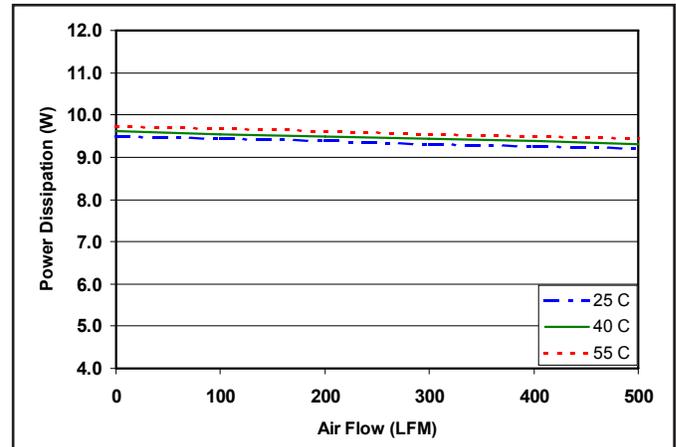


Figure 4: Power dissipation at nominal output voltage and 60% rated power vs. airflow rate for ambient air temperatures of 25°C, 40°C, and 55°C (nominal input voltage).

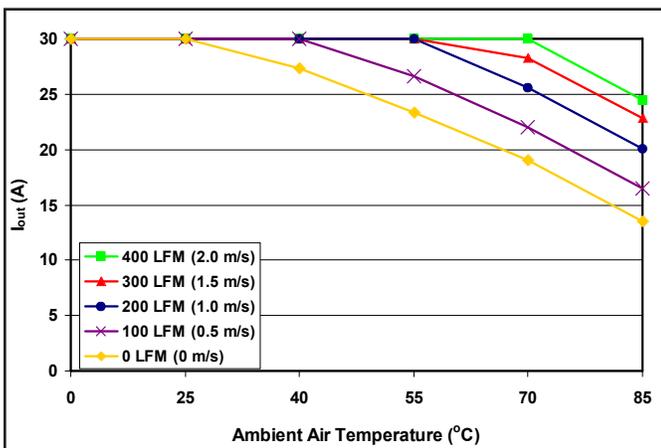


Figure 5: Maximum output power derating curves vs. ambient air temperature for airflow rates of 0 LFM through 400 LFM with air flowing from input to output (nominal input voltage).

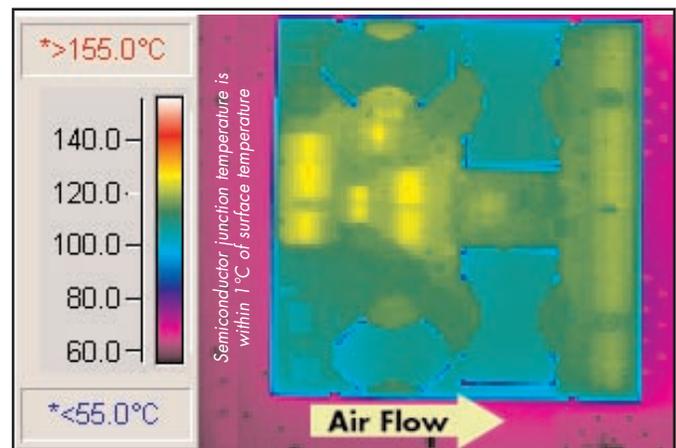
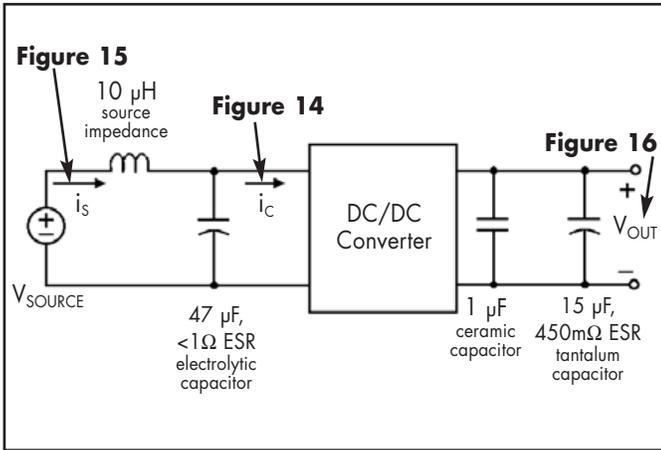


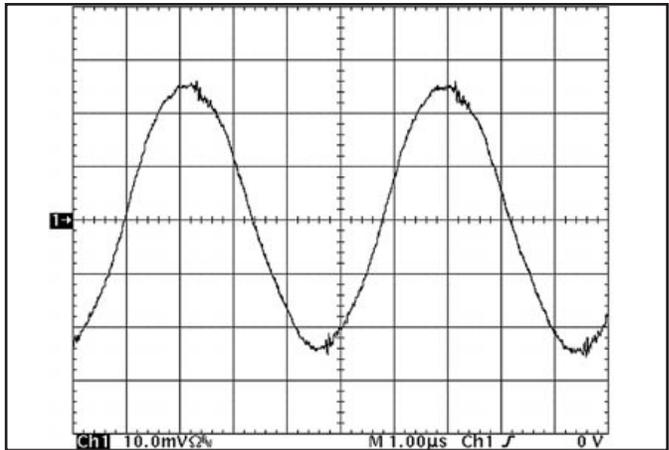
Figure 6: Thermal plot of converter at 30 amp load current with 55°C air flowing at the rate of 200 LFM. Air is flowing across the converter from input to output (nominal input voltage).

## Performance Curves

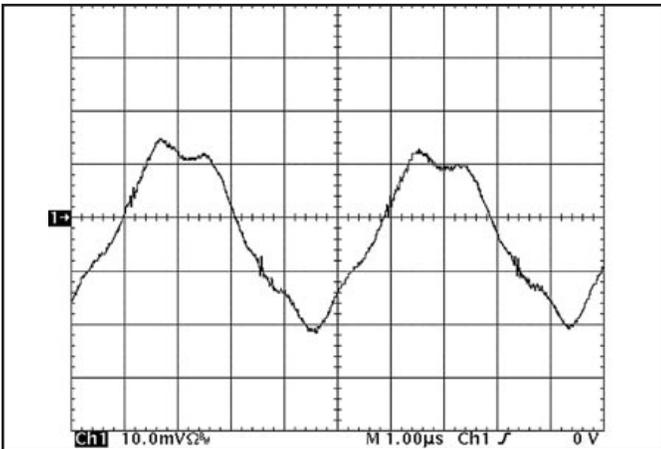
**Half Brick**     **48V<sub>in</sub> 5.3V<sub>out</sub> 30A**



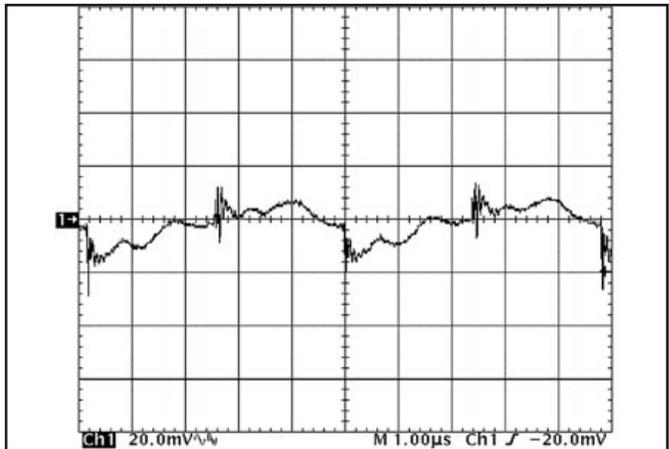
**Figure 13:** Test set-up diagram showing measurement points for Input Terminal Ripple Current (Figure 14), Input Reflected Ripple Current (Figure 15) and Output Voltage Ripple (Figure 16).



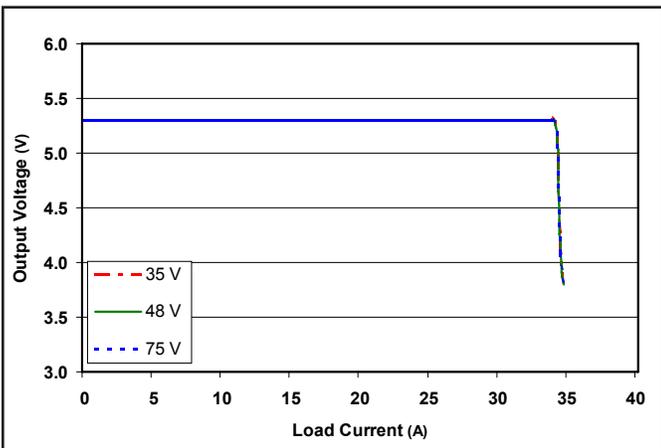
**Figure 14:** Input Terminal Ripple Current,  $i_c$ , at full rated output current and nominal input voltage with  $10\ \mu\text{H}$  source impedance and  $47\ \mu\text{F}$  electrolytic capacitor ( $100\ \text{mA}/\text{div}$ ). (See Figure 13)



**Figure 15:** Input reflected ripple current,  $i_s$ , through a  $10\ \mu\text{H}$  source inductor at nominal input voltage and rated load current ( $10\ \text{mA}/\text{div}$ ). (See Figure 13)



**Figure 16:** Output voltage ripple at nominal input voltage and rated load current ( $20\ \text{mV}/\text{div}$ ). Load capacitance:  $1\ \mu\text{F}$  ceramic capacitor and  $15\ \mu\text{F}$  tantalum capacitor. Bandwidth:  $20\ \text{MHz}$ . (See Figure 13)



**Figure 17:** Output voltage vs. load current showing typical current limit curves and converter shutdown points.

### BASIC OPERATION AND FEATURES

The PowerQor series converter uses a two-stage power circuit topology. The first stage is a buck-converter that keeps the output voltage constant over variations in line, load, and temperature. The second stage uses a transformer to provide the functions of input/output isolation and voltage step-down to achieve the low output voltage required.

Both the first stage and the second stage switch at a fixed frequency for predictable EMI performance. Rectification of the transformer's output is accomplished with synchronous rectifiers. These devices, which are MOSFETs with a very low on-state resistance, dissipate far less energy than Schottky diodes. This is the primary reason that the PowerQor converter has such high efficiency, even at very low output voltages and very high output currents.

Dissipation throughout the converter is so low that it does not require a heatsink for operation. Since a heatsink is not required, the PowerQor converter does not need a metal baseplate or potting material to help conduct the dissipated energy to the heatsink. The PowerQor converter can thus be built more simply and reliably using high yield surface mount techniques on a PCB substrate.

The PowerQor series of half-brick and quarter-brick converters uses the industry standard footprint and pin-out configuration.

### CONTROL FEATURES

**REMOTE ON/OFF (Pin 2):** The ON/OFF input, Pin 2, permits the user to control when the converter is on or off. This input is referenced to the return terminal of the input bus, Vin(-). There are two versions of the converter that differ by the sense of the logic used for the ON/OFF input.

In the positive logic version, the ON/OFF input is active high (meaning that a high turns the converter on). In the negative logic version, the ON/OFF signal is active low (meaning that a low turns the converter on). Figure A details five possible circuits for driving the ON/OFF pin. Figure B is a detailed look of the internal ON/OFF circuitry.

**REMOTE SENSE(+)** (Pins 8 and 6): The SENSE(±) inputs correct for voltage drops along the conductors that connect the converter's output pins to the load.

Pin 8 should be connected to Vout(+) and Pin 6 should be connected to Vout(-) at the point on the board where regulation is desired. A remote connection at the load can adjust for a voltage drop only as large as that specified in this datasheet, that is

$$[V_{out(+)} - V_{out(-)}] - [SENSE(+) - SENSE(-)] \leq \text{Sense Range \%} \times V_{out}$$

Pins 8 and 6 must be connected for proper regulation of the output voltage. If these connections are not made, the converter will deliver an output voltage that is slightly lower than its specified value.

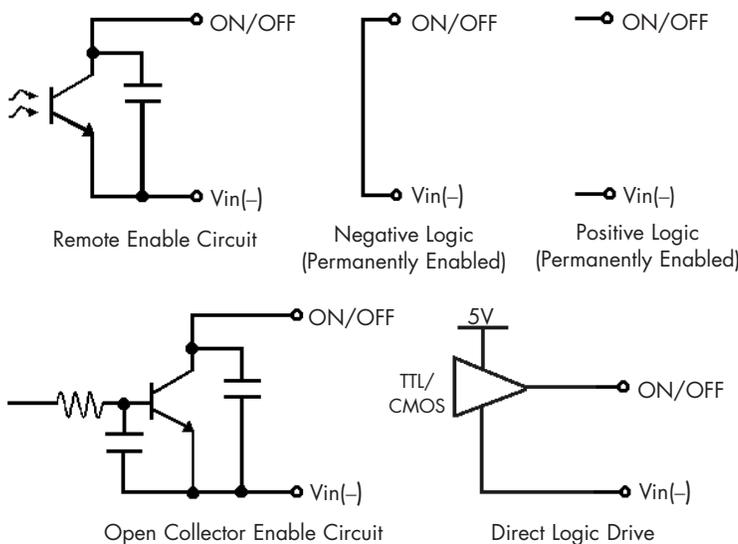


Figure A: Various circuits for driving the ON/OFF pin.

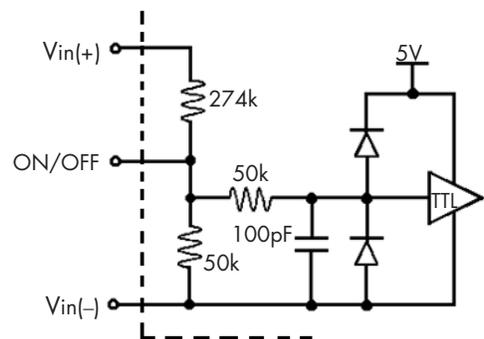


Figure B: Internal ON/OFF pin circuitry

**Note:** the output over-voltage protection circuit senses the voltage across the sense leads (pins 8 and 6) to determine when it should trigger, not the voltage across the converter's output pins (pins 9 and 5).

**OUTPUT VOLTAGE TRIM (Pin 7):** The TRIM input permits the user to adjust the output voltage across the sense leads up or down according to the trim range specifications.

To decrease the output voltage, the user should connect a resistor between Pin 7 and Pin 6 (SENSE(-) input). For a desired decrease of the nominal output voltage, the value of the resistor should be

$$R_{\text{trim-down}} = \left( \frac{100\%}{\Delta} \right) - 2 \text{ k}\Omega$$

where

$$\Delta\% = \left( \frac{V_{\text{nominal}} - V_{\text{desired}}}{V_{\text{nominal}}} \right) \times 100\%$$

To increase the output voltage, the user should connect a resistor between Pin 7 and Pin 8 (SENSE(+) input). For a desired increase of the nominal output voltage, the value of the resistor should be

$$R_{\text{trim-up}} = \frac{\left( \frac{V_{\text{nominal}}}{1.225} - 2 \right) \times V_{\text{DES}} + V_{\text{NOM}}}{V_{\text{DES}} - V_{\text{NOM}}} \text{ k}\Omega$$

Figure C graphs the relationship between the trim resistor value and R<sub>trim-up</sub> and R<sub>trim-down</sub>, showing the total range the output voltage can be trimmed up or down.

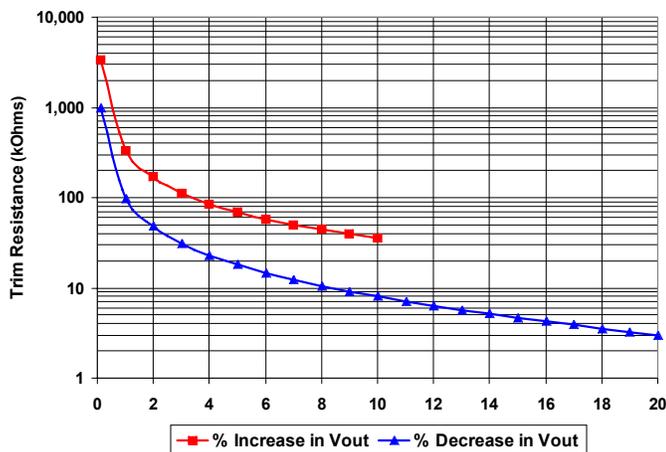


Figure C: Trim Graph for 5.3V<sub>out</sub> module

**Note:** the TRIM feature does not affect the voltage at which the

output over-voltage protection circuit is triggered. Trimming the output voltage too high may cause the over-voltage protection circuit to engage, particularly during transients.

**Total DC Variation of V<sub>out</sub>:** For the converter to meet its full specifications, the maximum variation of the DC value of V<sub>out</sub>, due to both trimming and remote load voltage drops, should not be greater than that specified for the output voltage trim range.

## PROTECTION FEATURES

**Input Under-Voltage Lockout:** The converter is designed to turn off when the input voltage is too low, helping avoid an input system instability problem, described in more detail in the application note titled "Input System Instability". The lockout circuitry is a comparator with DC hysteresis. When the input voltage is rising, it must exceed the typical Turn-On Voltage Threshold value (listed on the specification page) before the converter will turn on. Once the converter is on, the input voltage must fall below the typical Turn-Off Voltage Threshold value before the converter will turn off.

**Input Over-Voltage Shutdown:** Available on PQ48 models only. The converter turns off when the input voltage is too high, allowing the converter to withstand an input voltage as high as 100V without destruction. The shutdown circuitry is a comparator with DC hysteresis. When the input voltage exceeds the typical Input Over-Voltage Shutdown value, the converter will turn off. Once the converter is off, it will turn back on when the input voltage falls below the minimum Input Over-Voltage Shutdown value.

**Output Current Limit:** The maximum current limit remains constant as the output voltage drops. However, once the impedance of the short across the output is small enough to make the output voltage drop below the specified Output DC Current-Limit Shutdown Voltage, the converter turns off.

The converter then enters a "hiccup mode" where it repeatedly turns on and off at a 5 Hz (nominal) frequency with a 5% duty cycle until the short circuit condition is removed. This prevents excessive heating of the converter or the load board.

**Output Over-Voltage Limit:** If the voltage across the output pins exceeds the Output Over-Voltage Protection threshold, the converter will immediately stop switching. This prevents damage to the load circuit due to 1) excessive series resistance in output current path from converter output pins to sense point, 2) a release of a short-circuit condition, or 3) a release of a current limit condition. Load capacitance determines exactly how high the output voltage will rise in response to these conditions.

After 200 ms the converter will automatically restart.

**Over-Temperature Shutdown:** A temperature sensor on the converter senses the average temperature of the module. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensed location reaches the Over-Temperature Shutdown value. It will allow the converter to turn on again when the temperature of the sensed location falls by the amount of the Over-Temperature Shutdown Restart Hysteresis value.

below provides a diagram showing the internal input filter components. This filter dramatically reduces input terminal ripple current, which otherwise could exceed the rating of an external electrolytic input capacitor. The recommended external input capacitance is specified in the "Input Characteristics" section. More detailed information is available in the application note titled "EMI Characteristics" on the SynQor website.

### APPLICATION CONSIDERATIONS

**Input System Instability:** This condition can occur because any DC/DC converter appears incrementally as a negative resistance load. A detailed application note titled "Input System Instability" is available on the SynQor web site ([www.synqor.com](http://www.synqor.com)) which provides an understanding of why this instability arises, and shows the preferred solution for correcting it.

**Application Circuits:** Figure D below provides a typical circuit diagram which details the input filtering and voltage trimming.

**Input Filtering and External Capacitance:** Figure E

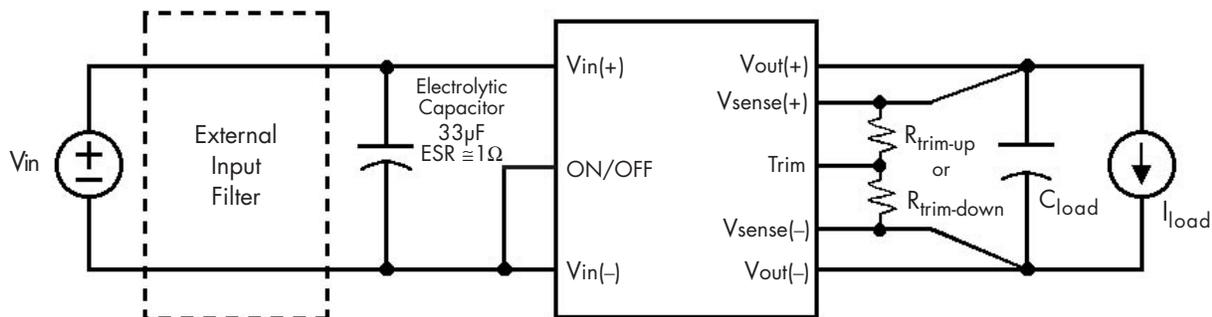


Figure D: Typical application circuit (negative logic unit, permanently enabled).

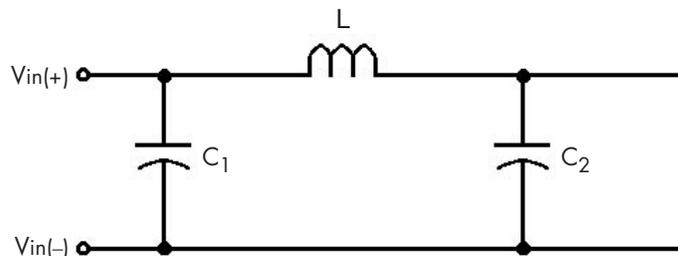


Figure E: Internal Input Filter Diagram (component values listed on page 3).

**Startup Inhibit Period:** The Startup Inhibit Period ensures that the converter will remain off for at least 200ms when it is shut down for any reason. When an output short is present, this generates a 5Hz "hiccup mode," which prevents the converter from overheating. In all, there are seven ways that the converter can be shut down, initiating a Startup Inhibit Period:

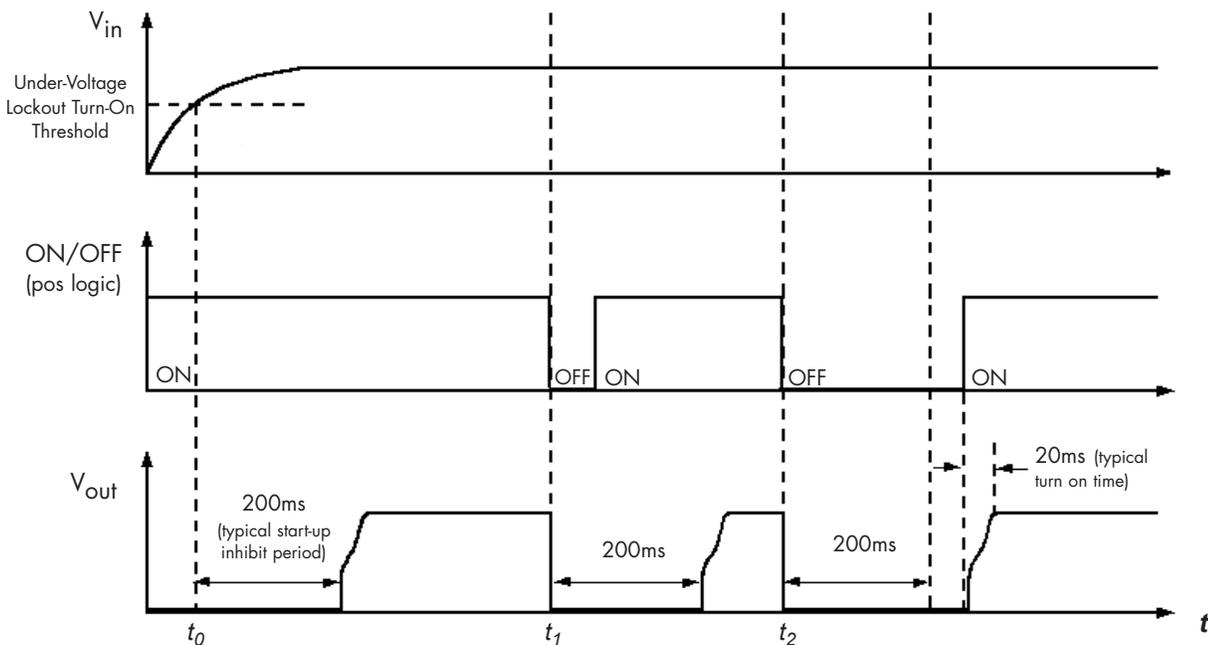
- Input Under-Voltage Lockout
- Input Over-Voltage Shutdown (not present in Quarter-brick)
- Output Over-Voltage Protection
- Over Temperature Shutdown
- Current Limit
- Short Circuit Protection
- Turned off by the ON/OFF input

Figure F shows three turn-on scenarios, where a Startup Inhibit Period is initiated at  $t_0$ ,  $t_1$ , and  $t_2$ :

Before time  $t_0$ , when the input voltage is below the UVL threshold, the unit is disabled by the Input Under-Voltage Lockout feature. When the input voltage rises above the UVL threshold, the Input Under-Voltage Lockout is released, and a Startup Inhibit Period is initiated. At the end of this delay, the ON/OFF pin is evaluated, and since it is active, the unit turns on.

At time  $t_1$ , the unit is disabled by the ON/OFF pin, and it cannot be enabled again until the Startup Inhibit Period has elapsed.

When the ON/OFF pin goes high after  $t_2$ , the Startup Inhibit Period has elapsed, and the output turns on within the typical Turn-On Time.



**Figure F: Startup Inhibit Period (turn-on time not to scale)**

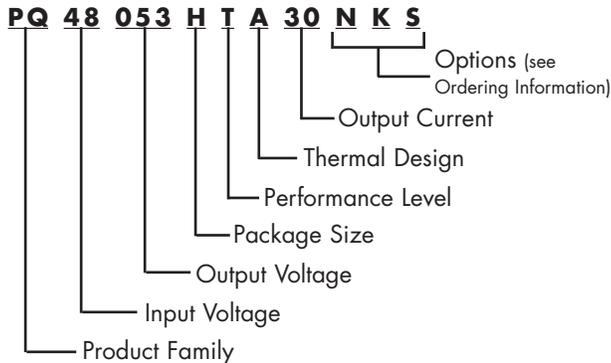


# Technical Specification

**Half Brick**     **48V<sub>in</sub> 5.3V<sub>out</sub> 30A**

## PART NUMBERING SYSTEM

The part numbering system for SynQor's PowerQor DC/DC converters follows the format shown in the example below.



## ORDERING INFORMATION

When ordering SynQor converters, please ensure that you use the complete 15 character part number consisting of the 12 character base part number and the additional 3 characters for options.

The 5.3V Tera half-brick is available in negative enable logic and standard feature set only.

Options Description: x y z		
Enable Logic	Pin Length	Feature Set
N - Negative	K - 0.110" N - 0.145" R - 0.180" Y - 0.250"	S - Standard

The first 12 characters comprise the base part number and the last 3 characters indicate available options. Although there are no default values for enable logic and pin length, the most common options are negative logic and 0.145" pins. These part numbers are more likely to be readily available in stock for evaluation and prototype quantities.

## Application Notes

A variety of application notes and technical white papers can be downloaded in pdf format at [www.synqor.com](http://www.synqor.com).

## Contact SynQor for further information and to order:

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**E-mail:** [power@synqor.com](mailto:power@synqor.com)  
**Web:** [www.synqor.com](http://www.synqor.com)  
**Address:** 155 Swanson Road  
 Olathe, KS 66061

**D5H9BHG**  
 SynQor DC/DC Converter  
 48V Input, 5.3V Output, 30A Output Current  
 Half Brick, Negative Enable Logic, Standard Feature Set

**K5FF5BHM**  
 SynQor DC/DC Converter  
 48V Input, 5.3V Output, 30A Output Current  
 Half Brick, Standard Enable Logic, Standard Feature Set