

# **Military COTS Non-Isolated DC-DC Converters**

9-90 V

0 - 90 V

26 A

Non-

Half-brick
DC-DC Converter

**Continuous Input** 

Output

The MCOTS NiQor® half-brick dc-dc converter is a non-isolated buck-boost regulator, which employs synchronous rectification to achieve extremely high conversion efficiency. The High Input Voltage MCOTS NiQor Brick family of converters can be used in traditional DPA (distributed power architecture) systems or provide a regulated output voltage from a battery source or other

variable voltage source. The MCOTS NiQor Brick family

can be configured to Buck the input voltage down to a

lower voltage or Boost the input voltage up to a higher

voltage using a single external resistor.

High efficiency, 97% at full rated load current
Delivers up to 26 A of output current
Input Voltage Range: 9-90 Vdc

• Extensive on-board input and output filtering

• Adjustable current limit with current monitor

**Protection Features** 

load current or short circuits

from damaging voltages

environmental conditions

voltage conditions

• Output Voltage Range: 0 - 90 V (negative output is possible)

• No minimum load requirement means no preload resistors required

• Input under-voltage lockout protects the converter at low input

• Input/output over-voltage protection protects load and regulator

• Over-current shutdown protects converter from excessive

Thermal shutdown protects converter from abnormal

**Operational Features** 

Current

Isolated

*Mil*COTS



Designed and manufactured in the USA.

### **Control Features**

- On/Off control
- Output voltage trim permits custom voltages
- Remote Sense
- Settable current limit
- Output voltage trim range of 0V 90V

### **Mechanical Features**

- Industry standard half-brick pin-out configuration
- Standard size: 2.49" x 2.39" (63.1mm x 60.6mm)
- Total height only 0.512" (13 mm)
- Total weight: Encased 5.6oz (158.2g)

### **Screening/Qualification**

- AS9100 and ISO 9001 certified facility
- Qualified to MIL-STD-810
- Available with S-Grade or M-Grade screening
- Pre-cap inspection per IPC-A-610, Class III
- Temperature cycling per MIL-STD-883, Method 1010, Condition B, 10 cycles
- Burn-In at 100 °C baseplate temperature
- Final visual inspection per MIL-STD-883, Method 2009
- Full component traceability

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MCOTS-N-28VE-90-HG Output: 9-90 V

**Current: 26A** 

## MCOTS-N-28VE-90-HG Electrical Characteristics

Ta = 25 °C, airflow rate = 300 LFM, Vin = 48 V dc unless otherwise noted; full operating temperature range is -55 °C to +100 °C baseplate temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Vout	Min.	Typ.	Max.	Units	Notes & Conditions
ABSOLUTE MAXIMUM RATINGS						
Input Voltage					0=	
Non-Operating	All	-1		100	V	Continuous
Operating	All			90	V	Continuous
Isolation Voltage						
Input to Baseplate				1150	Vdc	
Output to Baseplate				1150	Vdc	
Storage Temperature	All	-65		125	°C	
Voltage at ON/OFF input pin	All	0		5.5	V	
Voltage at Vset and Iset Pins		-0.2		3.5	٧	Note 1
Voltage at SyncIn Pin		-0.2		3.5	V	Note 1
Voltage between Vsense+ and +Vout pins				±6.0	V	
Voltage between Vsense- and -Vout pins				±0.25	V	
RECOMMENDED OPERATING CONDITIONS				-0120		
Input Voltage Range	All	9		90	V	Turn on at 10V
Input Fuse Rating	All			40	A	Fast blow external fuse recommended
Input Current	, 111			26	A	Input current max equals output rated current
External Input Capacitance	All	820		20	μF	ESR > 50 mΩ See Note 2
Output Voltage	All	0		90	V	ESIX > 50 HISE SEC NOTE 2
Output Voltage Output Current	All	0		26	A	Input voltage dependent
INPUT CHARACTERISTICS	All	0		20	Α	input voltage dependent
Input Under-Voltage Lockout						
Turn-On Voltage Threshold	All	9.2	9.5	10	V	
Turn-Off Voltage Threshold	All	5.1	5.5	5.9	V	
Lockout Hysteresis	All	3.1	4.0	3.3	V	
Input Current Limit	All		7.0	29.3	A	
No-Load Input Current	12		125	29.3	mA	
" "	48		125		mA	
D: 11 17 10 1	-					
Disabled Input Current	All		0.8		mA	
Input Filter Components Value (C\L\C)	All		10 \ 0.33 \ 36		μF\μH\μF	
OUTPUT CHARACTERISTICS						
Output Voltage Range	All	0		90	V	Set by Vset resistor
Operating Output Current Range	All	0		26	Α	
Output Voltage Regulation						
Load Regulation	All		*Vout*Iout/In			
Total Output Voltage Range	All	±100mV :	±2%*Vout + I	oad Reg		Between Sense pins, over sample, line, load, temp. & lif
Output Voltage Ripple and Noise (pk-pk/28Vin)	12		50		mV	28 Vin Full Load; 100uF; 20 MHz b.w.
"	48		400		mV	"
Output DC Over Current limit	All		29.3		Α	Effective on input, output condition
External Output Capacitance	All	100			μF	ESR > 1 m $\Omega$
Output Filter Components	All		40 \ 0.33 \ 13		μΕ\μΗ\μΕ	
Reverse Current	All		1		μA	Disabled
DYNAMIC CHARACTERISTICS						
Output Voltage during Current Transient						
For a Step Change in Output Current	12		1600		mV	(0.1 A/µs); 50%-75%-50% Iout max
Settling Time	12		400		us	To within 1.5% Vout nom.
For a Step Change in Output Current	48		2000		mV	(0.1 A/µs); 50%-75%-50% Iout max
Settling Time	48		1800		us	To within 1.5% Vout nom.
Turn on Transient Startup delay	All		2		ms	Resistive load
Turn on Transient Rise Rate	All		2.3		V/ms	"
Turn on Transient Output Voltage Overshoot	All		0		V	· w

# MCOTS-N-28VE-90-HG Output: 9-90 V

**Current: 26A** 

MCOTS-N-28VE-90-HG Electrical Characteristics (continued)

Ta = 25 °C, airflow rate = 300 LFM, Vin = 48 V dc unless otherwise noted; full operating temperature range is -55 °C to +100 °C baseplate temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Vout	Min.	Тур.	Max.	Units	Notes & Conditions
EFFICIENCY						
100% Load; 48 Vin	28		96		%	
100% Load; 72 Vin	48		97		%	
100% Load; 28 Vin	72		94		%	
50% Load; 48 Vin	28		96		%	
50% Load; 72 Vin	48		97		%	
50% Load; 28 Vin	72		94		%	
FEATURE CHARACTERISTICS						
Switching Frequency	All	240	250	260	KHz	
Synchronization						
Sync Frequency Range	All	200		300	KHz	
Duty Cycle Range at Input Pin	All	25		75	%	
Logic Low Threshold Range	All	0.8	1.2		V	
Logic High Threshold Voltage	All		1.3	2.0	V	
Threshold Hysteresis	All		0.1		V	
Pin Pull-Up Voltage	All		3.3		V	
Pin Pull-Up Resistance	All		5		kΩ	
On/Off, Negative (N) Logic						See REMOTE ON/OFF: in CONTROL FEATURES
Off-State Threshold Voltage	All		1.4	1.5	V	
On-State Threshold Voltage	All	1.0	1.1		V	
Threshold Hysteresis	All		0.3		V	
Pin Pull-Up Voltage	All		5		V	
Pin Pull-Up Resistance	All		25		kΩ	
Output Voltage Setpoint						See OUTPUT VOLTAGE SETPOINT: in CONTROL FEATURE
Pin Pull-Up Voltage	All		2.5		V	
Pin Pull-Up Resistance	All		10.9		kΩ	
Output Voltage Setpoint Range	All	0		90	V	
Output Over-Voltage Shutdown	All		99		V	Fixed - Does not vary with output setpoint
Output Current Setpoint				/-		See OUTPUT CURRENT SETPOINT: in CONTROL FEATURE
Pin Pull-Up Voltage	All		2.5		V	
Pin Pull-Up Resistance	All		10		kΩ	
Output Over Current Setpoint Range	All	0		29.3	Α	
Input/Output Current Limit	All	27.0	29.3	31.6	Α	
Ishare/Imon						See OUTPUT CURRENT SHARE: in CONTROL FEATURES
Pin Voltage at No Load	All		0.2		V	
Pin Voltage at Full Load (Imax)	All		2.2		V	
Pin Output Resistance	All		2.5		kΩ	
Over-Temperature Shutdown	All		115		°C	Average PCB Temperature
Over-Temperature Shutdown Restart Hysteresis	All		15		°C	
RELIABILITY CHARACTERISTICS						
Calculated MTBF per MIL-HDBK-217F	All		1.8		10 <sup>6</sup> Hrs.	Ground Benign, 70 °C Tb
Calculated MTBF per MIL-HDBK-217F	All		0.177		10 <sup>6</sup> Hrs.	Ground Mobile, 70 °C Tb
TEMP LIMITS FOR POWER DERATING						
Semiconductor Junction Temperature	All			125	°C	Package rated to 150 °C
Board Temperature	All			125	°C	UL rated max operating temp 130 °C
Baseplate Temperature	All			100	°C	l l l l l l l l l l l l l l l l l l l
			1	1 -00	1	I .

Note 1: All control signals are referenced to Vsense-pin.

Note 2: Input capacitance and ESR of input capacitance dictated by input stability requirements. For Vin << Nom Vin and/or Vout >> Nom Vout, consult SynQor Field Applications.

Please see SynQor Application Note: http://www.synqor.com/documents/appnotes/appnt\_System\_Instability.pdf

MCOTS-N-28VE-90-HG 005-0006591 Rev. H 09/09/2024

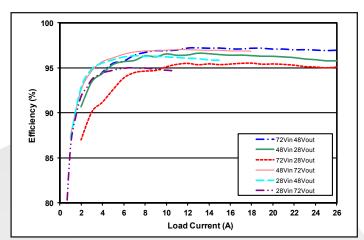


Figure 1: Efficiency at different output voltages vs. load current for different input voltages at 25°C.

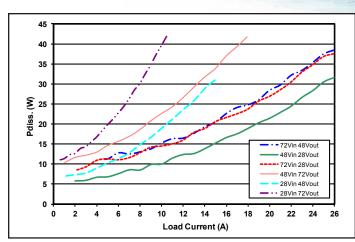


Figure 2: Power dissipation at different output voltages vs. load current for different input voltages at 25°C.

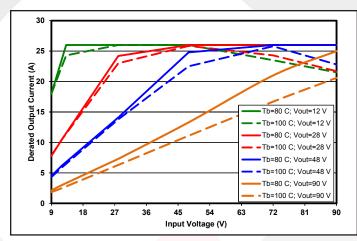


Figure 3: Maximum output power derating curve with a controlled baseplate temperature of 80°C and 100°C vs. Input voltage.

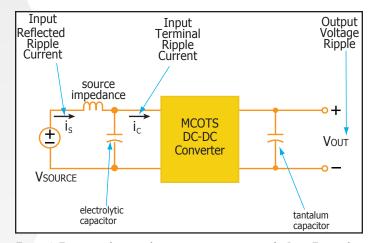


Figure 4: Test set-up diagram showing measurement points for Input Terminal Ripple Current (Fig 5) and Output Voltage Ripple (Fig 6).

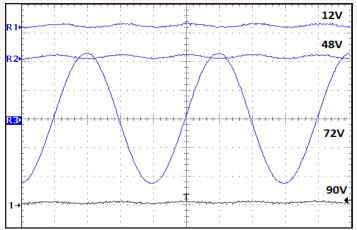


Figure 5: Input Terminal Ripple Current at 48V input and rated load current (50mA/div). Load capacitance: 100uF electrolytic cap. Bandwidth: 20MHz (See Figure 4). Timescale: 2 µs/div.

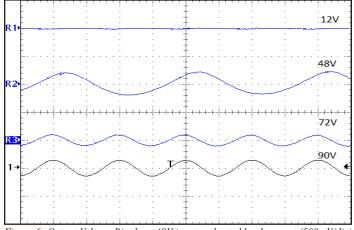


Figure 6: Output Voltage Ripple at 48V input and rated load current (500mV/div). Load capacitance: 100uF electrolytic cap. Bandwidth: 20MHz (See Figure 4). Timescale: 2 µs/div.

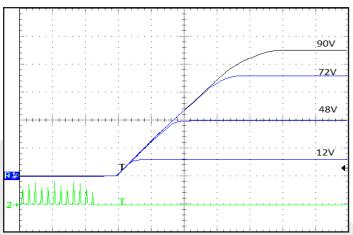


Figure 7: Turn-on transient at 28V input and full load. Top Traces: Vout (20V/div), Ch2: ON/OFF input (5V/div). Timescale: 10 ms/div.

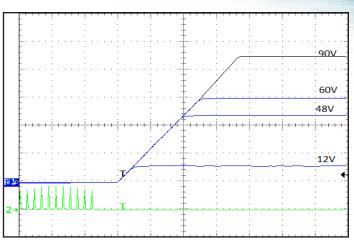


Figure 8: Turn-on transient at 28V input and zero load. Top Traces: Vout (20V/div), Ch2: ON/OFF input (5V/div). Timescale: 10 ms/div.

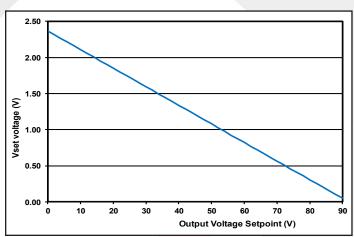


Figure 9: Vset pin voltage vs. Output Voltage Setpoint.

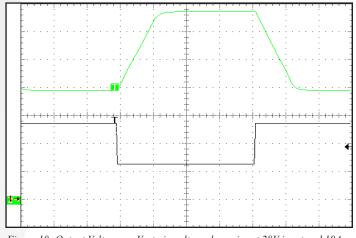


Figure 10: Output Voltage vs. Vset pin voltage dynamics at 28V input and 10A output. Ch2: Output Voltage (10V/div), Bottom trace: Vset pin voltage (500mV/div). Timescale: 5 ms/div.

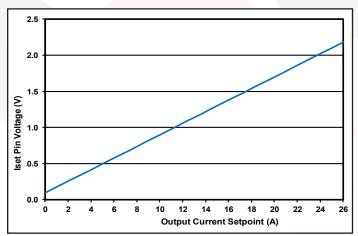


Figure 11: Iset pin voltage vs. Output Current Limit Setpoint.

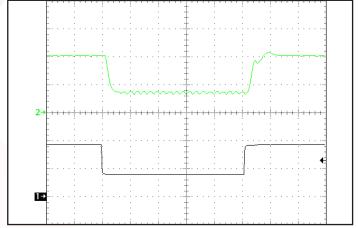


Figure 12: Output Current vs Iset pin voltage dynamics at 28V input and 10V output. Ch2: Output Current (5A/div), Ch1: Iset pin voltage (500mV/div). Timescale: 2 ms/div.

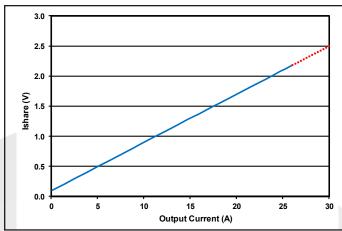


Figure 13: Ishare/Imon pin Voltage vs. Output Load Current.

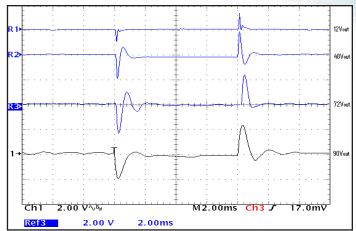


Figure 14: Output voltage response for 28V input; 12V, 48V, 72V, 90V output to step change in load current (50%-75%-50% of lout max; di/dt=0.1A/uS). Load cap: 100uF electrolytic cap, Vout (2V/div). Timescale: 2 ms/div.

Parameter	Notes & Conditions
STANDARDS COMPLIANCE	
CAN/CSA-C22.2 No.62368-1	
UL 62368-1	
FN 62368-1	

Note: An external input fuse must always be used to meet these safety requirements.

Contact SynQor for official safety certificates on new releases or download from the SynQor website.

### **BASIC OPERATION AND FEATURES**

This converter consists of integrated buck and boost converters, both controlled simultaneously by a digital controller. It automatically changes operating mode (buck mode or boost) when the line voltage or output set point changes. Very high efficiency is maintained over wide input and output ranges by shifting operational modes and use of synchronous rectifiers.

The converter runs at a fixed frequency with a predictable EMI performance.

This half-brick converter uses the industry standard footprint and pin-out configuration. A typical V-I characteristic, with Vsetpoint=50V and Isetpoint=18A, when operating from Vin=30V, is shown in Figure A.

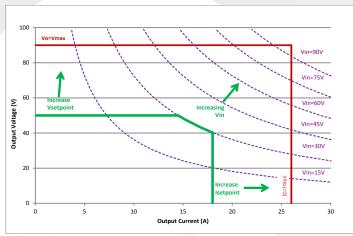


Figure A

### **CONTROL FEATURES**

**REMOTE ON/OFF:** Only Negative On/Off logic is available in the converter series: logic high at the input turns the converter Off while a logic low turns in On. Timing of this is shown in Figures 7, 8. A high level can be driven to any voltage between 1.5V and 3.3V, or simply left floating as the unit contains an internal  $25K\Omega$  pull-up to 5.0V. The pin can be pulled low (to Vsense-) by an optocoupler, an open-source/drain transistor or wired permanently to Vsense-. To provide noise immunity the input has 0.3V of hysteresis.

Multiple units that have a common Vsense- connection can be controlled by the same On/Off signal, but it is recommended that a small schottky diode be added to each input as shown in Figure B.

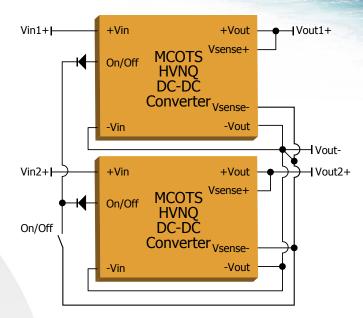
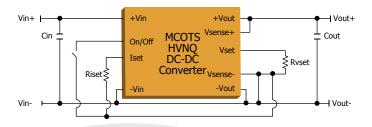


Figure B: Negative output setup

**CLOCK SYNCHRONIZATION:** The module will synchronize its switching to a clock signal at the SyncIn pin (relative to Vsense-) of between 200 and 300KHz. For noise immunity, the input has 0.1V hysteresis. It can be driven by any standard logic gate. The input has an internal  $5K\Omega$  pull-up to 3.3V; if unused, leave this input floating or tie it directly to Vsense-.

**OUTPUT VOLTAGE SETPOINT:** The output voltage can be programmed to any voltage between 0 V dc and Vmax by connecting one resistor between the Vset pin (6) and Vsense- (5); See Figure C. For a desired output voltage, the value of the resistor should be:

Rvset(Vset) = 
$$\left[ \left( \frac{11830 \times Vmax}{Vset + 0.058 \times Vmax} \right) -10912 \right] (\Omega)$$



### Figure C

Alternatively, the Vset pin can be driven from an external voltage source: Undriven, this pin floats at 2.5V which sets the output to 0V. See Figure 10 for the large scale dynamics of this input.

$$Vvset(Vset) = 2.366 - 2.316 \left( \begin{array}{c} \underline{Vset} \\ Vmax \end{array} \right)$$

See Figure 9

where:

Vset = desired output voltage setpoint Vmax = maximum rated output voltage (90V)

**OUTPUT CURRENT SETPOINT:** The maximum output current (effectively the current limit) can be reduced to any value between 0 and Imax by connecting one resistor between the Iset pin (B) and Vsense- (5); see Figure C. The value of the resistor should be:

RIset(Iset) = 
$$\left[ \left( \begin{array}{c} 0.0469 \text{ Imax} + \text{Iset} \\ \hline 1.153 \text{ Imax} - \text{Iset} \end{array} \right) *10200 - 10 \right] (\Omega)$$

Alternatively, the Iset pin can be driven from an external voltage source:

Viset(Iset)= 
$$\left(\begin{array}{c} 0.0953+2.085 * Iset/Imax \end{array}\right)$$
 V where:

Iset = desired output current setpoint Imax = maximum rated output current (26A)

Undriven, the Iset pin floats to 2.5V which sets the current limit at its nominal value of 115%\*Imax. See Figure 12 for the large signal dynamics of this control.

### **PROTECTION FEATURES**

**Input Under-Voltage Lockout:** The converter is designed to turn off when the input voltage is too low, helping avoid an input system instability problem, described in more detail in the application note titled "Input System Instability". The lockout circuitry is a comparator with DC hysteresis. When the input voltage is rising, it must exceed the typical Turn-On Voltage Threshold value (listed on the specification page) before the converter will turn on. Once the converter is on, the input voltage must fall below the typical Turn-Off Voltage Threshold value before the converter will turn off.

**Output Current Shutdown:** To provide protection in an output short condition, the unit is equipped with internal short circuit protection. When the short-circuit protection is triggered, the converter shuts down and then waits an inhibit time ( $\sim 100$ ms), after which it tries to turn on again. If the short condition remains, the current limit circuit will limit the output current. The unit will return to normal operation once the fault condition is removed.

**Internal Over-Voltage Protection:** To fully protect from excessive output voltage, the unit contains an output over-voltage shutdown that is fixed at  $\sim 110\%$  of Vmax. If this limit is reached, the converter shuts down and then waits an inhibit time ( $\sim 100$ ms), after which it restsarts.

**Over-Temperature Shutdown:** Two sensors in the module monitor the temperature of both the buck and boost sections. When the temperature at either sensor exceeds the Over Temperature Shutdown value, the converter is disabled. It will restart normally when it then cools by the amount of the Over-Temperature Shutdown Hysteresis.

### **APPLICATION CONSIDERATIONS**

**Input filtering:** These modules should be connected to a low-impedance source. A highly inductive source can affect the stability of the module. An input capacitor must be placed adjacent to the input pins of the module to minimize input ripple voltage and ensure stability. See SynQor's application note titled Input System Stability for guidance on selecting appropriate input filter elements.

### http://www.syngor.com/documents/appnotes/appnt\_System\_Instability.pdf

**Output capacitance:** An output capacitor of at least 100uF with an ESR of ~50mOhms is recommended to damp the resonance of the output filter. More capacitance can be added to improve dynamic response to load variation; there is not upper limit on the value of the output capacitance. Very large output capacitors may slow the converter's start up time due to the current required to charge them being limited by current limit or Isetpoint.

**Remote sense:** In the event of an open sense line, the module maintains output voltage regulation via internal resistors between its Vsense+ and +Vout, and Vsense- and -Vout pins. To avoid damaging those resistors, maintain the voltage differentials to within the limits in ABSOLUTE MAXIMUM RATINGS at all times.

**Current limit:** Available current is limited by maximum output current in buck mode (Vin>Vout), and by maximum input current in boost mode (Vin<Vout).

for Vin < Vout

where Imax = Maximum rated output current

Ilimit = 1.12 x Imax 
$$\left(\frac{\text{Vin}}{\text{Vout}}\right)$$
 (Amps)

**Generating Negative output voltage (inversion):** The circuit shown in Figure D can be used to generate a negative output voltage from a positive input.

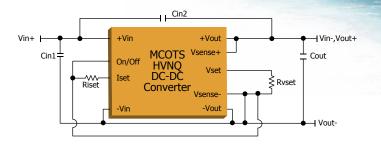


Figure D: Negative output setup

Note that all control signals are referenced to Vsense-, which in this arrangement is at Vout- potential.

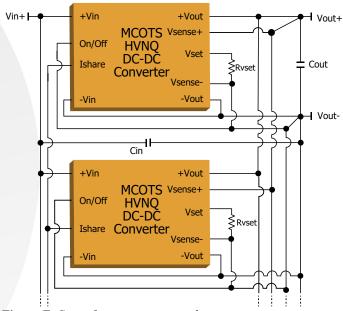


Figure E: Setup for output current sharing

**Current Sharing:** In applications requiring more power than can be supplied from a single converter, multiple units can be arranged to share the load as shown in Figure E.

The units should all be set at the same output voltage setpoint by using identical Rvset resistors. In this arrangement, the level of the Ishare/Imon bus is that of the average current delivered by each converter.

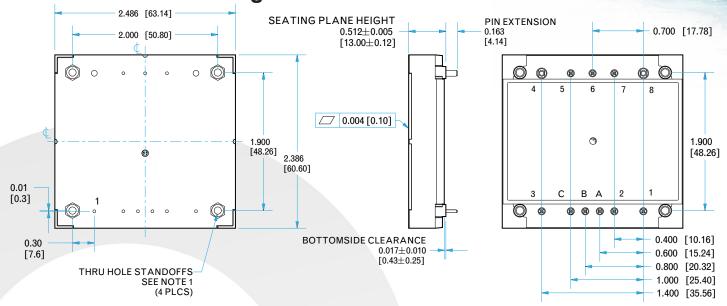
**Thermal Consideration:** The maximum operating baseplate temperature, TB, is 100 °C. Refer to the Thermal Derating Curves in the Technical Figures section to see the available output current at baseplate temperatures below 100 °C.

$$\mathbf{P}_{diss}^{\text{max}} = \frac{\mathsf{T}_{\mathsf{B}} - \mathsf{T}_{\mathsf{A}}}{\mathsf{R}_{\mathsf{T}}\mathsf{H}_{\mathsf{B}\mathsf{A}}}$$

A power derating curve can be calculated for any heatsink that is attached to the base-plate of the converter. It is only necessary to determine the thermal resistance, RTHBA, of the chosen heatsink between the baseplate and the ambient air for a given airflow rate. This information is usually available from the heatsink vendor. The following formula can be used to determine heatsink vendor. the maximum power the converter can dissipate for a given thermal condition if its base-plate is to be no higher than 100 °C.

This value of maximum power dissipation can then be used in conjunction with the data shown in the Power Dissipation Curves in the Technical Figures section to determine the maximum load current (and power) that the converter can deliver in the given thermal condition. For convenience, Thermal Derating Curves are provided in the Technical Figures section.

# Standard Mechanical Diagram



- 1)THREADED: APPLIED TORQUE PER M3 SCREW 6in-lb LIMIT. NONTHREADED: DIA 0.125" (3.18mm)
- 2)BASEPLATE FLATNESS TOLERANCE IS 0.004" (.10mm) TIR FOR SURFACE.
- 3)PINS 1-3, 5-7, A, B, and C ARE 0.040" (1.02mm) DIA. WITH 0.080" (2.03mm) DIA. STANDOFFS.
- 4)PINS 4 AND 8 ARE 0.080" (2.03mm) DIA. WITH 0.125" (3.18mm) DIA STANDOFFS
- 5)ALL PINS: MATERIAL: COPPER ALLOY

FINISH: MATTE TIN OVER NICKEL PLATE

6)WEIGHT: 5.6oz (158.2g)

7)ALL DIMENSIONS IN INCHES(mm)

TOLERANCES: X.XXIN +/-0.02 (X.Xmm +/-0.5mm)

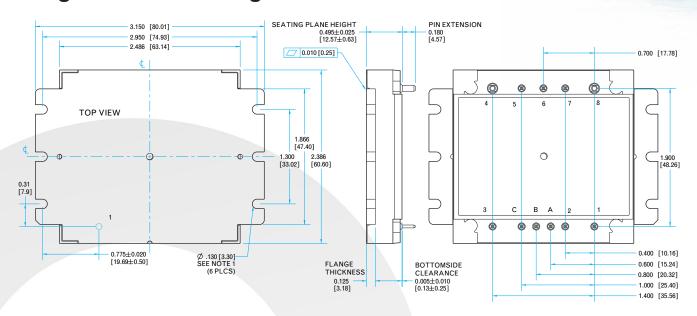
### **PIN DESIGNATIONS**

Pin	Label	Function, See Note 1
1	+Vin	Positive Supply Input
2	On/Off	Input to enable/disable the converter, TTL
Α	SyncIn	Input to synchronize the converter to an external clock, TTL
В	Iset	Input to set maximum output current
С	Ishare	Input/Output: Current monitor or Current share
3	-Vin	Negative Supply Input, internally connected to Pin 4
4	-Vout	Negative Power Output, internally connected to Pin 3
5	Vsense-	Negative Power Voltage Sense. See Note 2
6	Vset	Input to set the maximum output voltage.
7	Vsense+	Positive Output Voltage Sense. See Note 3
8	+Vout	Positive Power Output

#### Notes:

- 1)All Control signals are referenced to Vsense- pin.
- 2)Vsense- should be permanently connected to -Vout either at the converter or remotely.
- 3)Vsense+ should be permanently connected to +Vout either at the converter or remotely.

# Flanged Mechanical Diagram



### **NOTES**

- 1) APPLIED TORQUE PER M3 OR 4-40 SCREW 6in-lb LIMIT
- 2) BASEPLATE FLATNESS TOLERANCE IS 0.010" (.25mm) TIR FOR SURFACE.
- 3) PINS 1-3, 5-7, A, B, AND C ARE 0.040" (1.02mm) DIA. WITH 0.080" (2.03mm) DIA. STANDOFFS.
- 4) PINS 4 AND 8 ARE 0.080" (2.03mm) DIA. WITH 0.125" (3.18mm) DIA STANDOFFS
- 5) ALL PINS: MATERIAL: COPPER ALLOY

FINISH: MATTE TIN OVER NICKEL PLATE

- 6) WEIGHT: 5.8oz (164.2g)
- 7) ALL DIMENSIONS IN INCHES (mm)

TOLERANCES: X.XXIN +/-0.02 (X.Xmm +/-0.5mm)

X.XXXIN + -0.010 (X.XXmm + -0.25mm)

#### **PIN DESIGNATIONS**

Pin	Label	Function, see Note 1.
1	+Vin	Positive Supply Input.
2	ON/OFF	Input to enable/disable the converter, TTL.
Α	SyncIn	Input to synchronize the converter to an external clock, TTL.
В	Iset	Input to set the maximum output current.
С	Ishare	Input/Output: Current monitor or Current share.
3	-Vin	Negative Supply Input, internally connected to Pin 4.
4	-Vout	Negative Power Output, internally connected to Pin 3.
5	Vsense-	Negative Power Voltage Sense, See Note 2.
6	Vset	Input to set the maximum output voltage.
7	Vsense+	Positive Output Voltage Sense, See Note 3.
8	+Vout	Positive Power Output.

#### Notes:

- 1)All control signals referenced to Vsense- pin. or at the converter
- 2)Vsense+ should be permantly connected to +Vout either either at the convertor or remotely.
- 3)Vsense- should be permantly connected to -Vout either either at the convertor or remotely.

MCOTS-N-28VE-90-HG Output: 9-90 V

**Current: 26A** 

# **Qualification & Screening**

### **Mil-COTS Qualification**

Test Name	<b>Details</b>	# Tested (# Failed)	Consistent with MIL-STD-883F Method
Life Testing	Visual, mechanical and electrical testing before, during and after 1000 hour burn-in @ full load	15 (0)	Method 1005.8
Shock-Vibration	Visual, mechanical and electrical testing before, during and after shock and vibration tests	5 (0)	MIL-STD-202, Methods 201A & 213B
Humidity	+85 °C, 95% RH, 1000 hours, 2 minutes on / 6 hours off	8 (0)	Method 1004.7
Temperature Cycling	500 cycles of -55 °C to +100 °C (30 minute dwell at each temperature)	10 (0)	Method 1010.8, Condition A
Solderability	15 pins	15 (0)	Method 2003
DMT	-65 °C to +110 °C across full line and load specifications in 5 °C steps	7 (0)	
Altitude	70,000 feet (21 km), see Note	2 (0)	

Note: A conductive cooling design is generally needed for high altitude applications because of naturally poor convective cooling at rare atmospheres.

**Mil-COTS Converter and Filter Screening** 

Screening	Process Description	S-Grade	M-Grade	
<b>Baseplate Operating Temperature</b>		-55 °C to +100 °C	-55 °C to +100 °C	
Storage Temperature		-65 °C to +135 °C	-65 °C to +135 °C	
Pre-Cap Inspection	IPC-A-610, Class III	•	•	
Temperature Cycling	MIL-STD-883F, Method 1010, Condition		•	
Burn-In	100 °C Baseplate	12 Hours	96 Hours	
Final Electrical Test	100%	25 °C	-55 °C, +25 °C, +100 °C	
Final Visual Inspection	MIL-STD-883F, Method 2009	•	•	

Mil-COTS MIL-STD-810G Qualification Testing

MIL-STD-810G Test	Method	Description					
Fungus	508.6	Table 508.6-I					
Altitude	500.5 - Procedure I	Storage: 70,000 ft / 2 hr duration					
Aititude	500.5 - Procedure II	Operating: 70,000 ft / 2 hr duration; Ambient Temperature					
Rapid Decompression	500.5 - Procedure III	Storage: 8,000 ft to 40,000 ft					
Acceleration	513.6 - Procedure II	Operating: 15 g					
Salt Fog	509.5	Storage					
High Temperature	501.5 - Procedure I	Storage: 135 °C / 3 hrs					
nigii reiliperature	501.5 - Procedure II	Operating: 100 °C / 3 hrs					
Low Temperature	502.5 - Procedure I	Storage: -65 °C / 4 hrs					
Low reinperature	502.5 - Procedure II	Operating: -55 °C / 3 hrs					
<b>Temperature Shock</b>	503.5 - Procedure I - C	Storage: -65 °C to 135 °C; 12 cycles					
Rain	506.5 - Procedure I	Wind Blown Rain					
Immersion	512.5 - Procedure I	Non-Operating Non-Operating					
Humidity	507.5 - Procedure II	Aggravated cycle @ 95% RH (Figure 507.5-7 aggravated temp - humidity cycle, 15 cycles)					
Random Vibration	514.6 - Procedure I	10 - 2000 Hz, PSD level of 1.5 $g^2/Hz$ (54.6 $g_{rms}$ ), duration = 1 hr/axis					
Shock	516.6 - Procedure I	20 g peak, 11 ms, Functional Shock (Operating no load) (saw tooth)					
SHOCK	516.6 - Procedure VI	Bench Handling Shock					
Sinusoidal vibration	514.6 - Category 14	Rotary wing aircraft - helicopter, 4 hrs/axis, 20 g (sine sweep from 10 - 500 Hz)					
Sand and Dust	510.5 - Procedure I	Blowing Dust					
Sand and Dust	510.5 - Procedure II	Blowing Sand					

### **Ordering Information/ Part Numbering**

Example: MCOTS-N-28VE-90-HG

Not all combinations make valid part numbers, please contact SynQor for availability. See product summary page for details.

Fa	mily	Product	Input Voltage	Output Voltage	Package	Thermal Design	Screening Level
MC	COTS	N: Non-Isolated	<b>28V:</b> 9-60V <b>28VE:</b> 9-90V	<b>60:</b> 0-60V <b>90:</b> 0-90V	<b>HG:</b> Half Brick Giga	N:Normal Threaded D:Normal Non-Threaded F:Flanged	S: S-Grade M: M-Grade

#### **Application Notes**

A variety of application notes and technical white papers can be downloaded in pdf format from our website.

### **Contact SynQor for further information and to order:**

 Phone:
 978-849-0600
 Fax:
 978-849-0602

 E-mail:
 power@synqor.com
 Web:
 www.synqor.com

 Address:
 155 Swanson Road, Boxborough, MA 01719
 USA

<u>Warranty</u>

SynQor offers a two (2) year limited warranty. Complete warranty information is listed on our website or is available upon request from SynQor.

### **PATENTS**

SynQor holds numerous U.S. patents, one or more of which apply to most of its power conversion products. Any that apply to the product(s) listed in this document are identified by markings on the product(s) or on internal components of the product(s) in accordance with U.S. patent laws. SynQor's patents include the following:

7,765,687 7,787,261 8,149,597 8,644,027